

FYPs (2019/2020) in the Nanoelectronic Materials and Devices (NMD) Group, Tyndall National Institute, UCC

**Group of Prof Paul K. Hurley, team members
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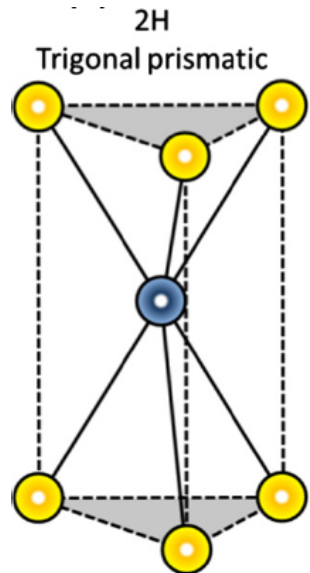
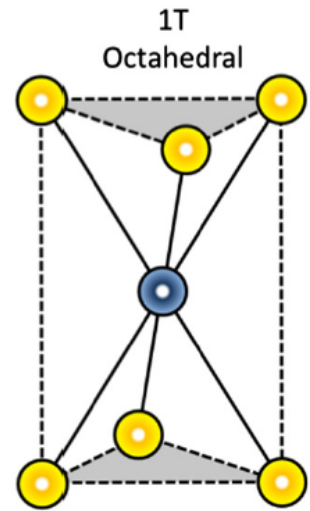
September 12th 2019

Outline

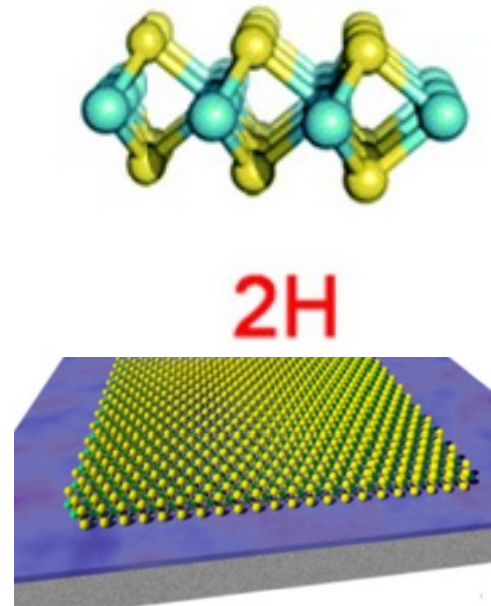
- Transition Metal Dichalcogenides (TMDs, 2D Materials)
- Characterisation methods (Circular TLM Structures, Back-Gated Junctionless Transistor, Van der Pauw 4-Point/Hall-effect) and equipment used...
- Previous FYP in 2018/2019 for TMDs with characterisation
- Up to 3 potential FYPs available in the NMD group, descriptions provided for each...

Transition Metal Dichalcogenides (TMDs, 2D Materials)

H																	He
Li	Be	<div>MX₂</div>										B	C	N	O	F	Ne
Na	Mg											Al	Si	P	S	Cl	Ar
K	Ca	Sc	Ti	V	Cr	Mn	Fe	Co	Ni	Cu	Zn	Ga	Ge	As	Se	Br	Kr
Rb	Sr	Y	Zr	Nb	Mo	Tc	Ru	Rh	Pd	Ag	Cd	In	Sn	Sb	Te	I	Xe
Cs	Ba	La-Lu	Hf	Ta	W	Re	Os	Ir	Pt	Au	Hg	Tl	Pb	Bi	Po	At	Rn
Fr	Ra	Ac-Lr	Rf	Db	Sg	Bh	Hs	Mt	Ds	Rg	Cn	Uut	Fl	Uup	Lv	Uus	Uuo

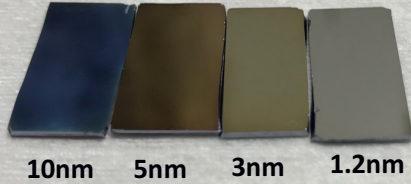


Group	M	X	Properties
4	Ti, Hf, Zr	S, Se, Te	Semiconducting ($E_g = 0.2\text{--}2\text{ eV}$). Diamagnetic.
5	V, Nb, Ta	S, Se, Te	Narrow band metals ($\rho \sim 10^{-4}\ \Omega\cdot\text{cm}$) or semimetals. Superconducting. Charge density wave (CDW). Paramagnetic, antiferromagnetic, or diamagnetic.
6	Mo, W	S, Se, Te	Sulfides and selenides are semiconducting ($E_g \sim 1\text{ eV}$). Tellurides are semimetallic ($\rho \sim 10^{-3}\ \Omega\cdot\text{cm}$). Diamagnetic.
7	Tc, Re	S, Se, Te	Small-gap semiconductors. Diamagnetic.
10	Pd, Pt	S, Se, Te	Sulfides and selenides are semiconducting ($E_g = 0.4\text{ eV}$) and diamagnetic. Tellurides are metallic and paramagnetic. PdTe_2 is superconducting.

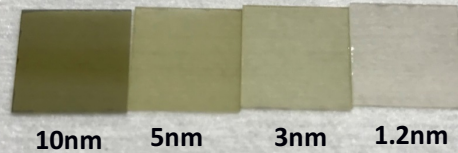


Characterisation methods (TLM, BG-JLT, 4P/Hall)

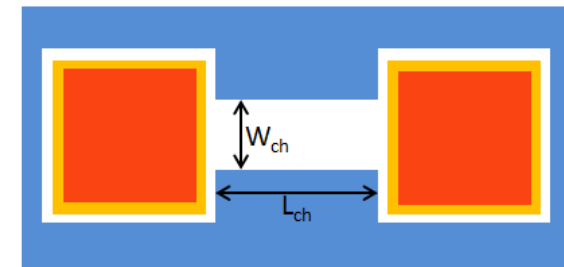
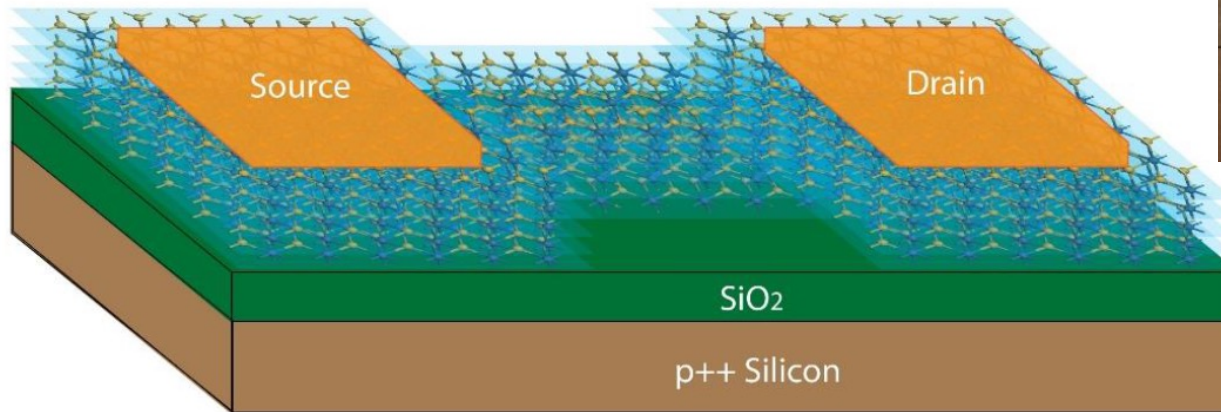
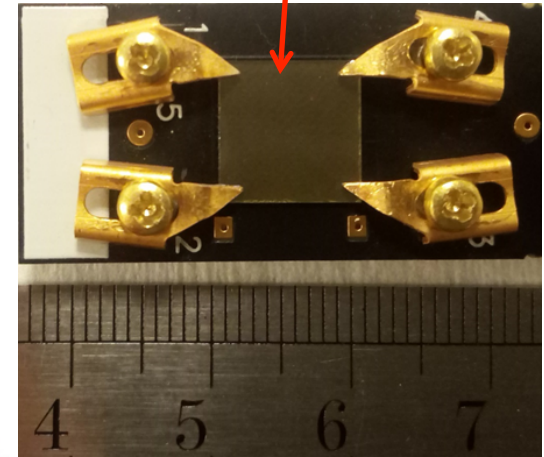
MoS₂ on SiO₂/Si



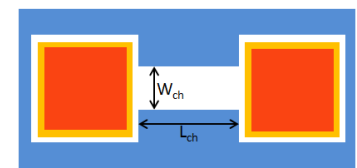
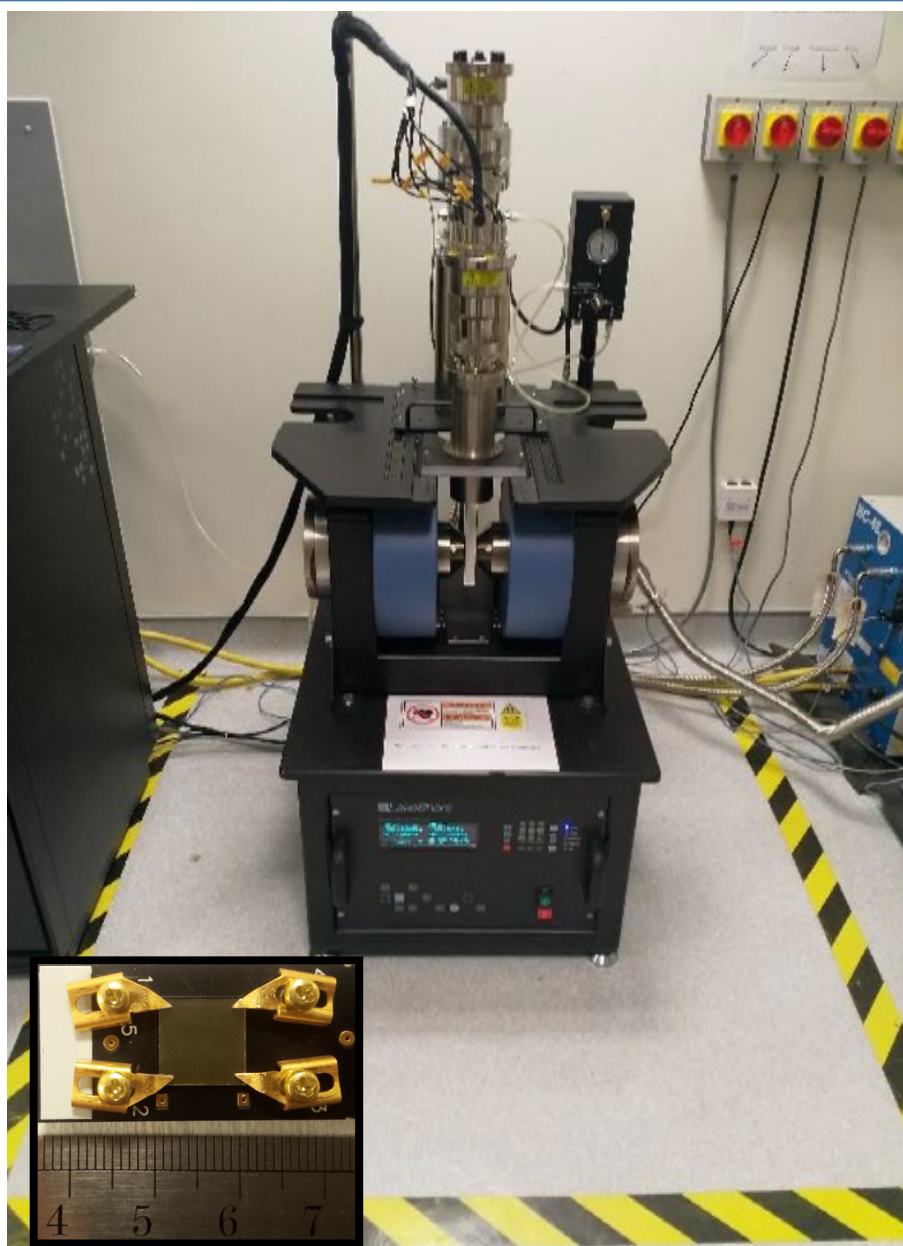
MoS₂ on sapphire



MoS₂ deposited on sapphire
or Al₂O₃/sapphire substrate



Equipment: 2P/4P/Hall System; Cascade Probe Station



Previous FYP in 2018/2019

“Study of Transition Metal Dichalcogenides for Back-End-Of-Line Switching Applications”, Department of Physics, PY4115.

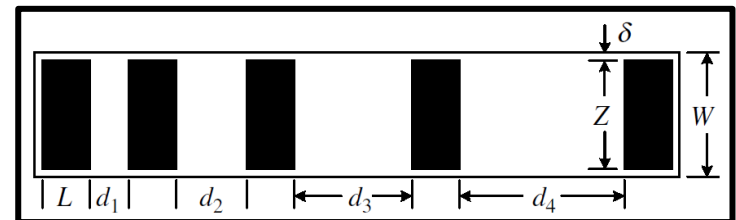
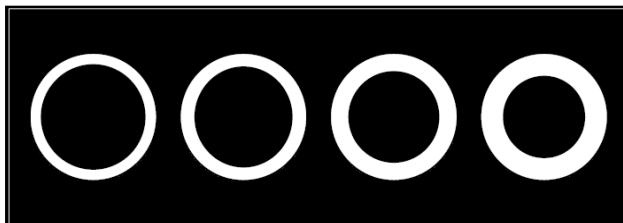
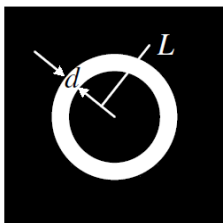
- Low thermal budget MoS₂ and PtSe₂ were grown and analyzed in different ways for their semiconductor characteristics:-
 - MoS₂ analyzed by 4-point and Hall-effect measurements
 - PtSe₂ by MOSFET analysis if thin-film modulation occurred
- 1. It was discovered that MoS₂ when doped with rhenium or niobium gives *n*-type or *p*-type material, respectively, and that non-intentionally doped MoS₂ is a defective *p*-type semiconductor.
- 2. PtSe₂ is known to be a semi-metal in bulk form and at ~4-5nm thickness we confirmed this behavior, but at a thickness of ~2.5-3 nm we discovered that PtSe₂ is in fact a *p*-type semiconductor.
- 3. Rhenium doping may additionally electrically stabilize MoS₂.
- 4. Suitable for back-end-of-line logic, memory and sensor circuitry for integrated More-than-Moore and Beyond-CMOS functionality.

Up to 3 potential FYPs available in the NMD group

- Transition Metal Dichalcogenides for all FYPs below:-
 - FYP 1: Circular TLM assessment
 - FYP 2: Back-Gated Junctionless Transistor characterisation
 - FYP3: Van der Pauw 4-Point/Hall-effect analysis

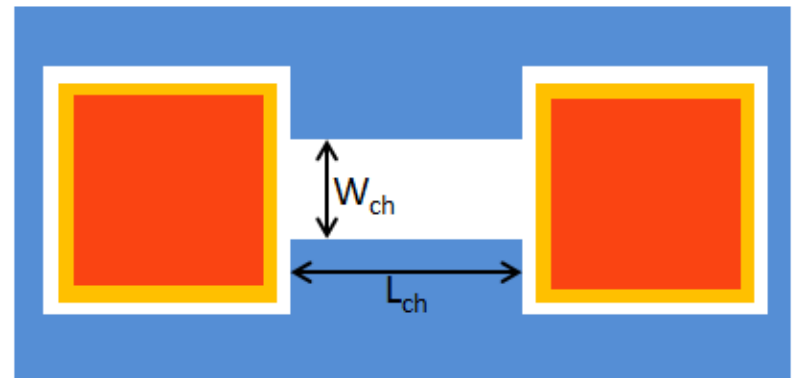
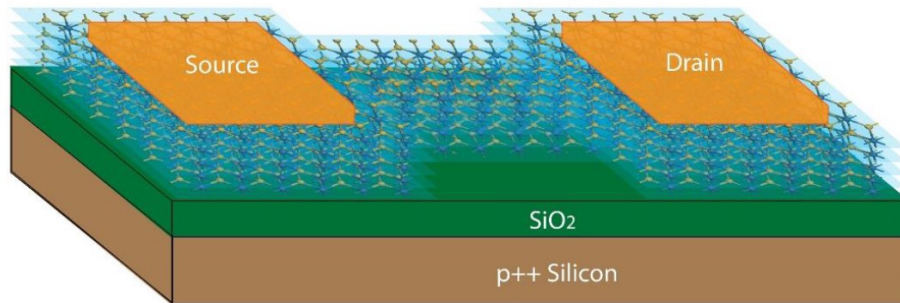
FYP 1: Circular TLM assessment

- FYP 1 Description: TLM (the transmission line model, and/or the transfer length method) structures are made in a circular design (and/or in a linear design if feasible), using a TMD semiconductor such as MoS_2 , WSe_2 , etc. on SiO_2/Si substrates (a plan B in the event of processing problems would be to use a III-V semiconductor such as $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$). The TLM devices are then analysed by IV assessment on our Cascade Probe Station and total resistance versus distance is plotted. From these plots we can then extract the resistance components for the semiconductor: (1) sheet resistance, (2) contact resistance, (3) specific contact resistivity, and (4) transfer length.



FYP 2: Back-Gated Junctionless Transistor assessment

- FYP 2 Description: Back-gated junctionless transistor (BG-JLT) structures will be formed using a TMD semiconductor such as MoS_2 , WSe_2 , etc. on SiO_2/Si substrates (a plan B in the event of processing problems would be to use a III-V semiconductor such as $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$). The BG-JLT devices are then analysed by $I_{\text{ds}}-V_{\text{ds}}$ and $I_{\text{ds}}-V_{\text{bg}}$ assessment on our Cascade Probe Station to determine field-effect modulation and performance. These output and transfer characteristics can potentially determine field-effect mobility, threshold voltage, subthreshold slope, etc.



FYP 3: Van der Pauw 4-Point/Hall-effect analysis

- FYP 3 Description: Van der Pauw (VdP) structures will be formed using a TMD semiconductor such as MoS_2 , WSe_2 , etc. on an insulating substrate (a plan B in the event of processing problems would be to use a III-V semiconductor such as $\text{In}_{0.53}\text{Ga}_{0.47}\text{As}$). The VdP devices are then analysed by 2-point, 4-point and Hall-effect measurements on our Lakeshore Hall Measurement System (HMS) which has very broad I.R=V capability and an electromagnet powered for a range of $\sim \pm 1.7$ Tesla in DC mode and up to $\sim +1.24$ Tesla RMS in AC mode. We will determine semiconductor resistivity, contact resistance, Hall coefficient, carrier concentration, dominant carrier type and Hall-effect mobility.

